International Rectifier

HiRel™ INT-A-Pak 2, PLASTIC HALF-BRIDGE IGBT MODULE

G450HHBK06P2

Product Summary

Part Number	V _{CE}	lc	V _{CE(SAT)}
G450HHBK06P2	600V	450A	1.8



The HiRel™ INT-A-Pak series are isolated near hermetic power modules which combine the latest IGBT and Soft Recovery Rectifier Technology. The module uses both high-speed and low Vce(sat) IGBT's packaged for ultra low thermal resistance junction to case. The G450HHBK06P2 power module consists of six IGBT's and six FRED's in a Phase- Leg or Half-Bridge configuration.

Features:

- Rugged, Lightweight near Hermetic Package with Integrated Power Terminal Cap
- Gen IV IGBT Technology
- Soft Recovery Rectifiers
- Ultra-Low Thermal Resistance
- Zener Gate Protection
- Very Low Conduction and Switching Loss
- -55°C to +125°C Operating Temperature
- Screening to meet the intent of MIL-PRF-38534 Class H
- Short Circuit Capability
- 2.0 Ohms Series Gate Resistor
- High Altitude Operation, 85,000 Feet Above Sea Level at Rated Voltage

Absolute Maximum Ratings @ Tj=25°C (unless otherwise specified)

Parameter	Symbol	Value	Units
Collector-to-Emitter Voltage	V _{CES}	600	V
Gate-to-Emitter Voltage	V _{GE}	±20	· · · · · · · · · · · · · · · · · · ·
Continuous Collector Current @ Tc = 25°C			^
Continuous Collector Current @ Tc = 70°C	1 'c [450	Α
Isolation Voltage	V _{ISOL}	2500	V _{RMS}

G450HHBK06P2

Electrical Characteristics @ Tj = 25°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Off Characteristics			-			
Collector Emitter Breakdown Voltage	V _{CES}	V _{GE} = 0V	600	-	-	V
Zero Gate Voltage Collector Current	ICES	V _{GE} =0V, V _{CE} = 600V	-	-	2.0	mA
Gate Emitter Leakage Current	I _{GES}	V _{GE} = ±15V, V _{CE} = 0V	-	-	10	μA
On Characteristics	-,-	- · · · · · · · · · · · · · · · · · · ·				
Gate Threshold Voltage	V _{GE(TH)}	V _{CE} = V _{GE} , I _C = 45mA	4.0	-	7.5	V
Collector Emitter Saturation Voltage		V _{GE} = 15V, I _C = 450A	-	1.8	2.6	
Dynamic Characteristics						
Total Gate Charge	Qg	V _{CF} = 300V, I _C = 450A, V _{GF} = 15V		2,600	-	nC
Input Capacitance	C _{IES}	THE SOUTH OF THE SOUTH		48	-	
Output Capacitance	C _{OES}	V _{GE} = 0V, V _{CE} = 25V, f = 1.0MHz		3.0	-	nF
Reverse Transfer Capacitance	C _{RES}			0.3	-	
Switching Inductive Load Chara	octoristic	c				
Turn-On Delay Time	td(on)	<u> </u>		800	900	
Rise Time	tr			460	700	ns
Turn-On Losses	Eon	V _{CC} = 300V, I _C = 450A, V _{GF} =15V	-	45	-	mJ
Turn-Off Delay Time	td(off)	$R_{G(on)} = 20Ω$, $R_{G(off)} = 10Ω$, L=100μH		2800	3400	
Fall Time	tf	S(SII)	_	400	500	ns
Turn-Off Losses	E _{off}		-	60	-	mJ
Diode Characteristics						
Forward Voltage	V _F	I _F = 450A	_	1.2	1.8	V
Reverse Recovery Charge	Qrr			9.5	12	μC
Peak Reverse Recovery Current	Irr	V _R =300V, I _C =450A, di/dt =-1100A/μs	-	105	- 12	Α
Reverse Recovery Time	trr		_	160	170	ns

2 www.irf.com

International IOR Rectifier

G450HHBK06P2

Electrical Characteristics @ Tj = 125°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Off Characteristics						
Collector Emitter Breakdown Voltage	V _{CES}	V _{GE} = 0V	600	-	-	٧
Zero Gate Voltage Collector Current	ICES	V _{GE} =0V, V _{CE} = 600V	-	-	18	mA
Gate Emitter Leakage Current	I _{GES}	V _{GE} = ±15V, V _{CE} = 0V	-	-	10	μΑ
Gate Threshold Voltage	V _{GE(TH)}	V _{CE} = V _{GE} , I _C = 45mA	4.0	-	7.5	
On Characteristics						
Collector Emitter Saturation Voltage		V _{GE} = 15V, I _C = 450A	-	1.8	2.6	V
Diode Characteristics						
Forward Voltage	V _F	I _F = 450A	-	1.2	1.8	>

Thermal-Mechanical Specifications

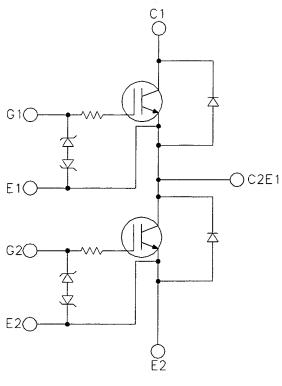
Parameter	Symbol	Min	Max	Units
IGBT Thermal Resistance, Junction to Case, per Switch	R _{thJC}	-	0.07	•cw
Diode Thermal Resistance, Junction to Case, per Switch		-	0.12]
Operating Junction Temperature Range	TJ	-55	150	- °c
Storage Temperature Range	T _{STG}	-55	125] '
Screw Torque - Mounting	7	·	26	in-lbs
Screw Torque - Terminals	'	<u>-</u>	20	111-105
Module Weight		-	270	g

Module Screening

Test or Inspection	MIL-STD-883		Comments	
	Method	Condition		
Internal Visual	2017			
Temperature Cycle	1010	В	10 Cycles, -55°C to +125°C	
Mechanical Shock	2002	В	1500G, 0.5ms, 5 Times (Y1 direction only)	
Burn-in	1015	Α	160 Hrs @ +125°C	
Final Electrical Test			Group A, -55°C, +25°C, +125°C	
External Visual	2009			

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Schematic



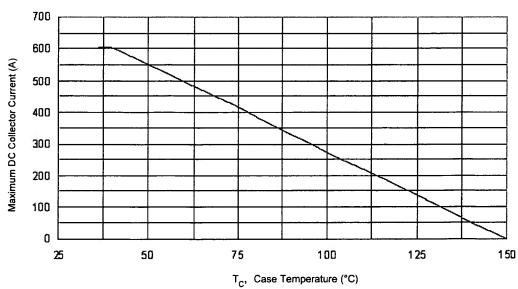
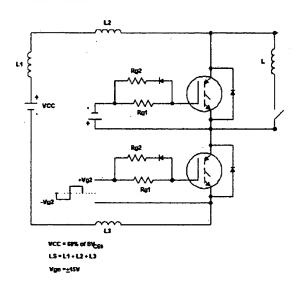


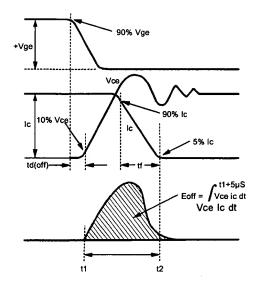
Fig 1: Maximum Collector Current Vs Case Temperature

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G450HHBK06P2



 $\begin{aligned} \textbf{Fig. 2} &- \textbf{Test Circuit for Measurement of} \quad \textbf{E}_{\text{on}}, \\ &\quad \textbf{E}_{\text{off}}, \textbf{t}_{\text{rr}}, \textbf{Q}_{\text{rr}}, \textbf{I}_{\text{rr}}, \textbf{t}_{\text{d(on)}}, \textbf{t}_{\text{r}}, \textbf{t}_{\text{d(off)}}, \textbf{t}_{\text{f}} \end{aligned}$



 $\mbox{\bf Fig. 3 - Test Waveforms for Circuit of Fig. 2,} \\ \mbox{\bf Defining } \mbox{\bf E}_{\mbox{off}}, \mbox{\bf t}_{\mbox{d}(\mbox{off})}, \mbox{\bf t}_{\mbox{f}}$

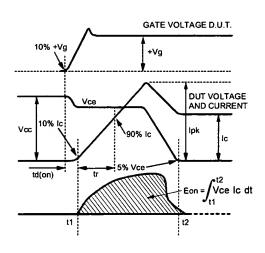
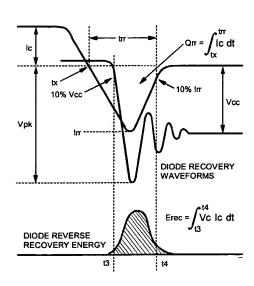
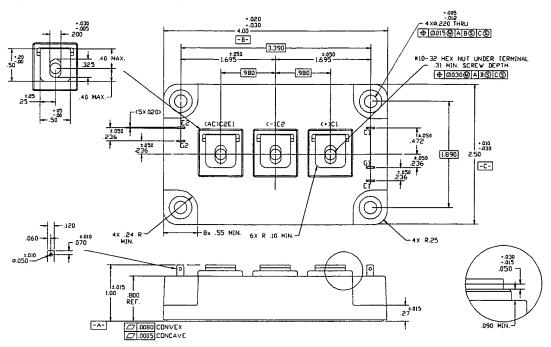


Fig. 3 - Test Waveforms for Circuit of Fig. 2, Defining $\mathsf{E}_{\mathsf{on}},\,\mathsf{t}_{\mathsf{d}(\mathsf{on})},\,\mathsf{t}_{\mathsf{r}}$

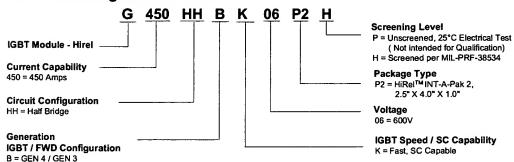


Case Outline and Dimensions - HiRel™ INT-A-Pak 2



Notes: 1) All dimensions are in inches
2) Unless otherwise specified,
Tolerances .XX = ±0.01, .XXX = ±0.005

Part numbering Nomenclature



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WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, Tel: (310) 252-7105 IR LEOMINSTER: 205 Crawford St., Leominster, Massachusetts 01453, Tel: (978) 534-5776 Data and specifications subject to change without notice. 11/05